

**Emeritus Professor:** 

Electrical & Computer Engineering; Neuroscience

**Emeritus Adjunct Professor:** 

Material Science & Engineering; Philosophy

Past Associate Director: MRC Institute 1996-2011 Past Director: Graduate Program in Neuroscience Past Affiliate: Dept. of Physiology & Biophysics, University of Washington School of Medicine Director: Wells Laboratory for Computational

Neuroscience & Mental Physics

Past Associate Chair: Electrical & Computer Engineering

Retired Registered Professional Engineer (Idaho) Past Senior Member IEEE

Life Member Sigma Xi

B. S. 1975, Iowa State (Electrical Engineering) M.S. 1978, Stanford (Electrical Engineering)

Total all types: 171

Ph.D. 1985 University of Idaho

## **Employment History**

- 1975-1986 Development Engineer, The Hewlett Packard Company
- 1986-1989 Project Leader, The Hewlett Packard Company
- 1989-1991 Production Engineering Manager, The Hewlett Packard Company
- 1991-1993 Project Manager (R&D), The Hewlett Packard Company
- 1993-2013 Faculty Member, The University of Idaho. Emeritus 2013

## **Publication Record**

Refereed Journal and Conference Papers: 62
 On-Line Books: 19

Professional Meeting Papers: 12 On-Line Papers & Tutorials: 41
Patents: 4 Other papers/presentations: 32

Patents: 4
Textbooks: 1

Graduate Students Supervised: 48 Other Graduate Student Committees: 73

**Professional Interests** 

• Development of social-natural sciences

- Public education
- Mathematical & computer modeling of systems
- Mathematical & computer modeling of neuron physiology
- Theory of biological neural networks and mind-brain theory
- Neural networks and Embedding Field Theory
- Set Membership Theory
- Biological and Adaptive Signal Processing
- Kant's Critical Philosophy and the Critical Theory of the Phenomenon of Mind

## Classes and Teaching

- Undergraduate and graduate electronics
- Undergraduate and graduate signal processing & systems theory
- Undergraduate and graduate coding and information theory
- Communication and Control Systems Theory
- Biological Signal Processing

home page address: <a href="http://www.mrc.uidaho.edu/mrc/people/rwells/">http://www.mrc.uidaho.edu/mrc/people/rwells/</a>

alternate home page: <a href="http://www.ece.uidaho.edu/mrc/people/rwells">http://www.ece.uidaho.edu/mrc/people/rwells</a>

web publications: <a href="http://www.mrc.uidaho.edu/mrc/people/rwells/techdocs">http://www.mrc.uidaho.edu/mrc/people/rwells/techdocs</a> VIVO Link at UI Library: <a href="http://vivo.nkn.uidaho.edu/vivo/display/n89636">http://vivo.nkn.uidaho.edu/vivo/display/n89636</a>